Reg.No. Image: SidDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS) M.Tech I Year I Semester Regular & Supplementary Examinations February 2018 MICRO CONTROLLERS & INTERFACING (Common to CS, PE and ES) Time: 3 hours Max. Marks: 60 (Answer all Five Units 5 X 12 = 60 Marks) Image: Image
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OR
6 a Give a brief note on on-chip counter, serial I/O. 7M
b What is watchdog timer? What should be done to set up watchdog timer before to use it?
UNIT-IV
b Explain the different Instructions of ARM processor and explain each
instruction with example. 8M
OR
b What do you mean by ARM? Explain about memory organization of ARM 7M
9 a Explain about Serial Communication Controller7Mb Explain the specifications of RS232 / RS4355M
OR
10 a Explain the major levels of embedded system design process with an example 8M b Explain the case study of Automotive application 4M *** END ***